

<b>AMENDMENT TRANSMITTAL LETTER</b>				Docket No. 02008/071003	
Application No. 10/779,904-Conf. #9608		Filing Date February 17, 2004		Examiner O. A. Louie	
				Art Unit 2436	

Applicant(s): Masahiro Ishida et al.

Invention: METHOD AND APPARATUS FOR DEFECT ANALYSIS OF SEMICONDUCTOR INTEGRATED CIRCUIT

**TO THE COMMISSIONER FOR PATENTS**

Transmitted herewith is an amendment in the above-identified application.  
The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED						
	Claims Remaining After Amendment	Highest Number Previously Paid		Number Extra Claims Present	Rate	
Total Claims	12	- 20	=	0	x 52.00	0.00
Independent Claims	3	- 3	=	0	x 220.00	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>						
Other fee (please specify):						
<b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:</b>						0.00

☒ Large Entity
 ☐ Small Entity

☒ No additional fee is required for this amendment.

☐ Please charge Deposit Account No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_.

☐ A check in the amount of \$ \_\_\_\_\_ to cover the filing fee is enclosed.

☐ Payment by credit card.

☒ The Director is hereby authorized to charge and credit Deposit Account No. 50-0591 as described below.

☒ Credit any overpayment.

☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

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Dated: March 12, 2010